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John Colter, Netscape Navigator

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» Key

IEEE JNL IEEE Journal or Magazine

IEEE JNL IEEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEEE CNF IEEE Conference Proceeding

IEEE STD IEEE Standard

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Dharmasena, H.P.; Vaidyanathan, R.;  
[Parallel and Distributed Processing, 1999. 13th International and 10th Symposium on Parallel and Distributed Processing, 1999. IPPS/SPDP. Proceedings](#)  
12-16 April 1999 Page(s):21 - 25  
Digital Object Identifier 10.1109/IPPS.1999.760429  
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- ☐ 2. **B<sup>mad</sup>-tree: an efficient data structure for parallel processing**  
Das, S.K.; Demuynck, M.A.;  
[Parallel and Distributed Processing, 1996. Eighth IEEE Symposium on](#)  
23-26 Oct. 1996 Page(s):384 - 391  
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- ☐ 3. **The reconfigurable ring of processors: fine-grain tree-structured computations**  
Rosenberg, A.L.; Scarano, V.; Sitaraman, R.K.;  
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Volume 46, Issue 10, Oct. 1997 Page(s):1119 - 1131  
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- ☐ 4. **State-optimal snap-stabilizing PIF in tree networks**  
Bui, A.; Datta, A.K.; Petit, F.; Villain, V.;  
[Self-Stabilizing Systems, 1999. Proceedings. 19th IEEE International Conference on Distributed Computing and Workshop on](#)  
31 May-4 June 1999 Page(s):78 - 85  
Digital Object Identifier 10.1109/SLFSTB.1999.777490  
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- ☐ 5. **Design of a superscalar processor based on queue machine computation model**  
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**6. Parallel power flow calculation in electric distribution networks**

Fukuyama, Y.; Nakanishi, Y.; Hsiao-Dong Chiang;

[Circuits and Systems, 1996. ISCAS '96. 'Connecting the World'. 1996 IEEE International Symposium](#)

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[Parallel Processing Symposium, 1995. Proceedings. 9th International](#)

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Howells, M.C.; Agarwal, V.K.;

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- ☐ **13. Optimal image computations on reduced VLSI architectures**  
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- ☐ **14. NETRA: a hierarchical and partitionable architecture for computer vision systems**  
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- ☐ **15. Generating an efficient broadcast sequence using reflected Gray codes**  
Moore, J.A.; Quinn, M.J.;  
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- ☐ **17. Lower bounds on the loading of multiple bus networks for binary tree algorithms**  
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- ☐ **18. Comparative study of strategies for formal verification of high-level processors**  
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Taubin, A.; Fant, K.; McCardle, J.;  
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- ☐ **21. A prefix code matching parallel load-balancing method for solution-adaptive unstructured fi on distributed memory multicomputers**  
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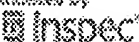
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- ☐ **25. A hierarchical floorplanning approach**  
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- ☐ **26. Optimal evaluation of vector expression trees**  
 Rauber, T.;  
Information Technology, 1990. 'Next Decade in information Technology'. Proceedings of the 5th Joint Conference on (Cat. No. 90TH0325-9)  
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- ☐ **27. A reconfigurable and hierarchical parallel processing architecture: performance results for :**  
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- ☐ **28. Hierarchical star-controlled rings: scalable reliable networks for multiprocessors**  
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- ☐ **29. Assignment and scheduling communicating periodic tasks in distributed real-time systems**  
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- ☐ **30. Adaptive scheduling across a distributed computation platform**  
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Araujo, G.; Malik, S.; Mike Tien-Chien Lee;

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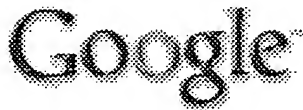
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reading a **leaf** pattern when the **leaf** of a corresponding **search tree** is reached ... A network **processor** provides the basis for a wide range of solutions from ...

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cent performance improvements in **processor** hardware also ... B-tree creation, traditionally fill each **leaf** to the desired. storage utilization (eg, 90%) and ...

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**tree**. We use the word "**processor**" to denote peer-to-peer. nodes, in order to avoid confusion ... the distributed **tree** to reach appropriate **leaf** nodes. The ...

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Since the **processor** is much faster, it is more important to minimize the ... B+ **tree**. Each internal node/**leaf** is designed to fit into one I/O block of data. ...

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When a **processor** joins, it navigates the **tree** to find a. **processor** with high load, ... If the **leaf** is a remote region, a request is routed to the peer to ...

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Citations: Efficient locking for concurrent operations on b-trees ...

As mentioned earlier, we assume that all indices are B trees [23] in which all # key, RID # entries are in the **leaf** pages. The inner nodes of the B **tree** ...

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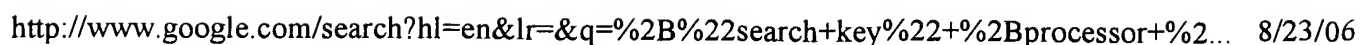
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struct and store a B+ **search tree** on top of the sequence of. node records. Value

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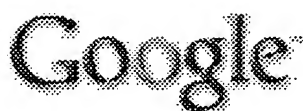
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%RMU-I-BTRROODBK, root dbkey of B-tree is 47:563:0. Although the **leaf** ... in the selection expression for the COUNT statement generates a **search key** that ...[download.oracle.com/otn\\_hosted\\_doc/rdb/pdf/rdbrn\\_711.pdf](http://download.oracle.com/otn_hosted_doc/rdb/pdf/rdbrn_711.pdf) - [Similar pages](#)Free! Speed up the web. [Download the Google Web Accelerator.](#) [Search within results](#) | [Language Tools](#) | [Search Tips](#) | [Dissatisfied? Help us improve](#)[Google Home](#) - [Advertising Programs](#) - [Business Solutions](#) - [About Google](#)

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Look-up is performed with the aid of a **tree** search engine (TSE) 70. The TSE 70 performs control **memory** 72 accesses, enabling the protocol **processor** 40 to ...

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Longest prefix match (LPM) algorithm implementation for a network ...

The data contained in a **leaf** is application dependent and its size or **memory** requirements are defined by the LUDefTable entry for the **tree**. FIG. ...

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[PPT] binary search tree

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B+ **tree**. Each internal node/leaf is designed to fit into one I/O block of data. ... first one or two levels of the B+-**tree** are usually kept in main **memory**. ...

[www.cs.ust.hk/~huamin/COMP171/btree1.ppt](http://www.cs.ust.hk/~huamin/COMP171/btree1.ppt) - [Similar pages](#)

[PDF] B-tree indexes, interpolation search, and skew Abstract 1 Introduction

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70%), it is not uncommon that all non-**leaf** pages of an ac- tive B-**tree** index remain permanently in main **memory** and. that read operations for these non-**leaf** ...

[www.cs.cmu.edu/~damon2006/pdf/graefe06btreeindexes.pdf](http://www.cs.cmu.edu/~damon2006/pdf/graefe06btreeindexes.pdf) - [Similar pages](#)

[PDF] Cache-Conscious Concurrency Control of Main-Memory Indexes on ...

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100 **processor** clocks [Sun97]. For the shared-**memory** multiprocessor system, which ... the **leaf** node of the B+-**tree** propagates to the upper level ...

[www.dia.uniroma3.it/~vldbproc/022\\_181.pdf](http://www.dia.uniroma3.it/~vldbproc/022_181.pdf) - [Similar pages](#)

Citations: Cache conscious indexing for decision-support in main ...

Further, the disparity between **processor** speed and **memory** latency is only ... the B **tree** uses for its **leaf** format) yields poor cache performance since the ...

[citeseer.ist.psu.edu/context/1230969/369006](http://citeseer.ist.psu.edu/context/1230969/369006) - 35k - [Cached](#) - [Similar pages](#)

[PDF] Abstract Introduction

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Between 1970 and 2004, **processor** speed, on-chip caches and main **memory** have all ... which may exist in a **leaf** page (a B-**tree** of order 2 will have at least 2 ...

[2004.eurobsdcon.org/uploads/media/EBSD04\\_35.pdf](http://2004.eurobsdcon.org/uploads/media/EBSD04_35.pdf) - [Similar pages](#)

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**Tree**-based index structures proposed for use in database. systems include ISAM, B ... **search key**. Suppose that all of these **leaf**-level sequences ...

[research.microsoft.com/~jrzhou/pub/simd.pdf](http://research.microsoft.com/~jrzhou/pub/simd.pdf) - [Similar pages](#)

[PDF] **B-tree** Indexes and CPU Caches

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**memory** is no longer valid either. If some **processor** at- ... entries in a **B-tree** leaf or separators in an interior node of. a **B-tree**. ...

doi.ieeecomputersociety.org/10.1109/ICDE.2001.914847 - [Similar pages](#)

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R/W between cache and **processor**: 1MG with <=10 nanoseconds ... However, unlike a standard **B+tree**, here the **leaf** nodes store the actual ...

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